

TITLE: METHOD AND APPARATUS FOR AUTOMATIC DETECTION OF A SERIAL PERIPHERAL INTERFACE  
(SPI) DEVICE MEMORY SIZE  
INVENTOR(S): Lane Huack and Timothy D. Jordan  
USSN: 09/778,233 ATTORNEY DOCKET #: CYPR-CD00205



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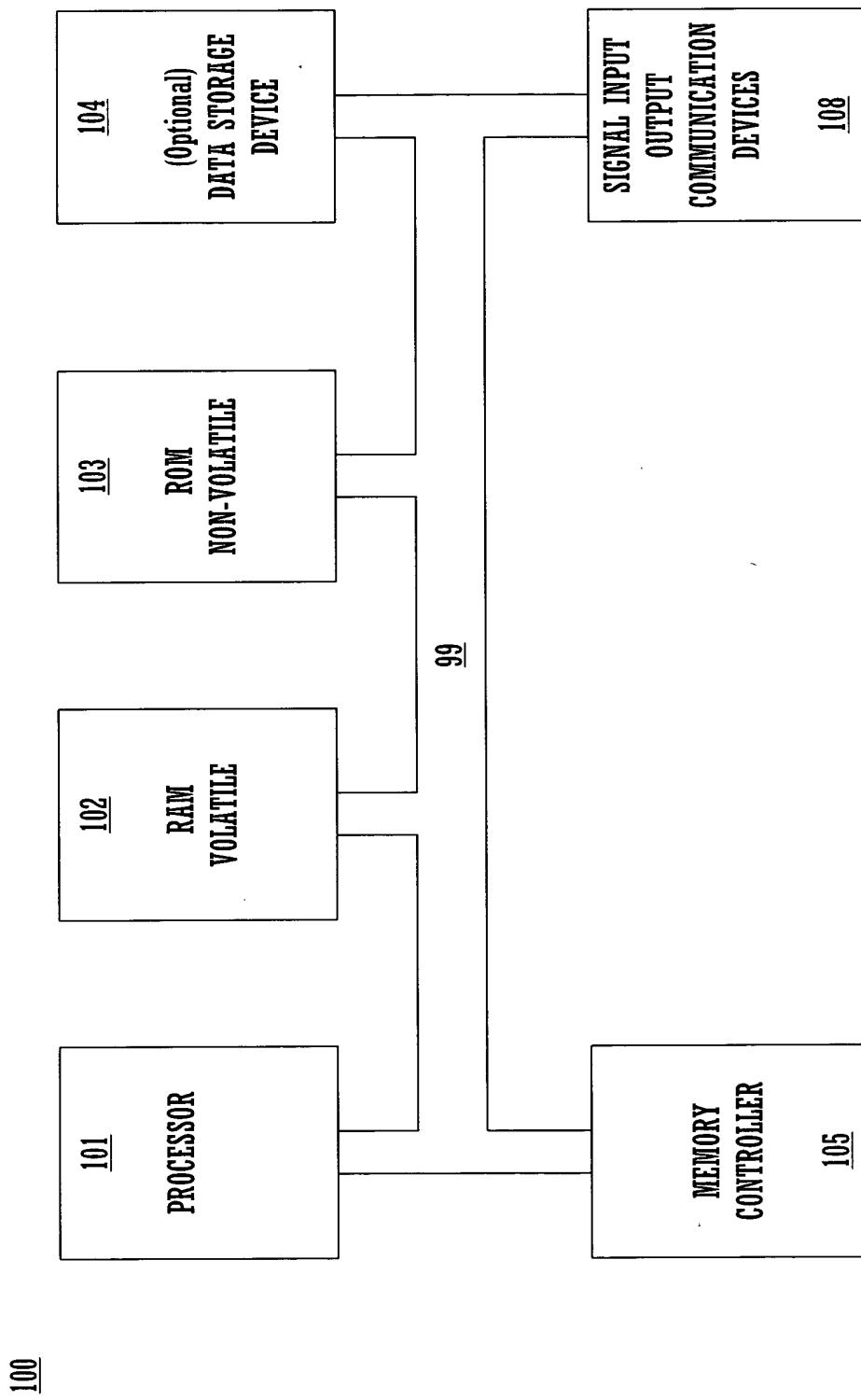


FIGURE 1

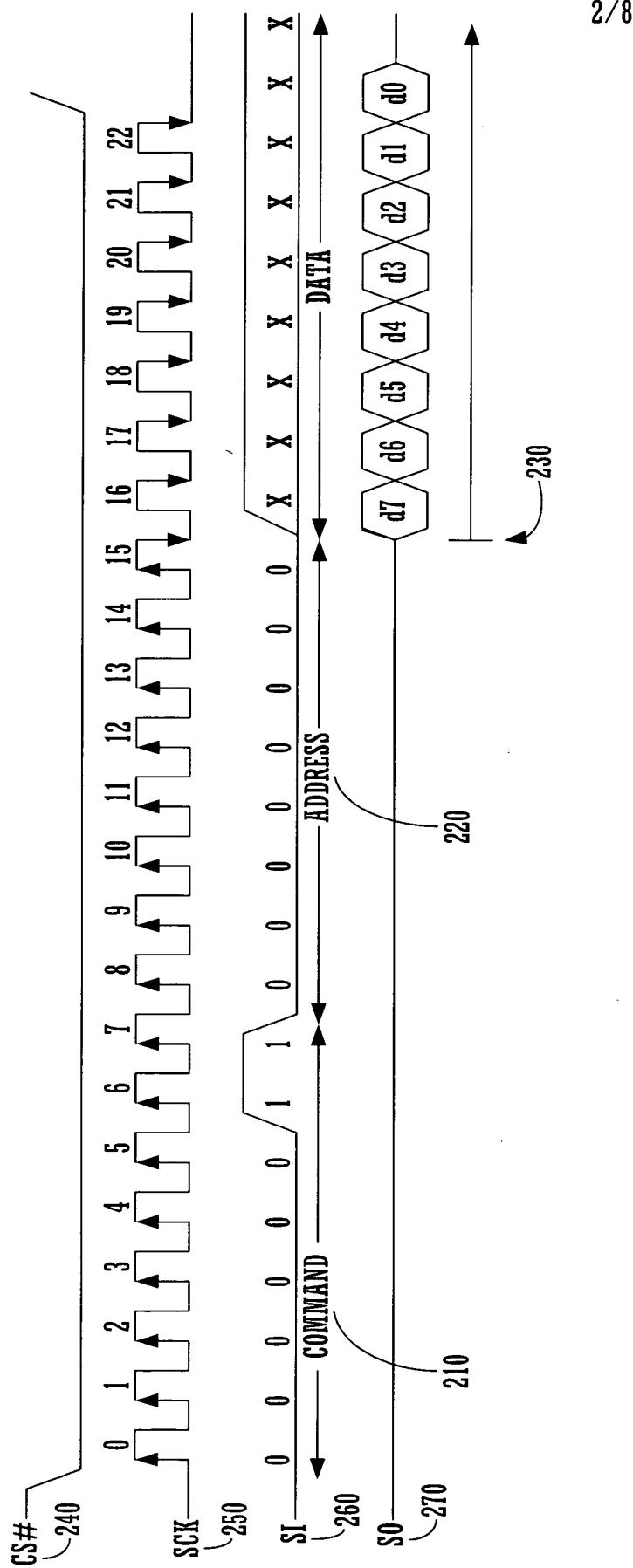


FIGURE 2

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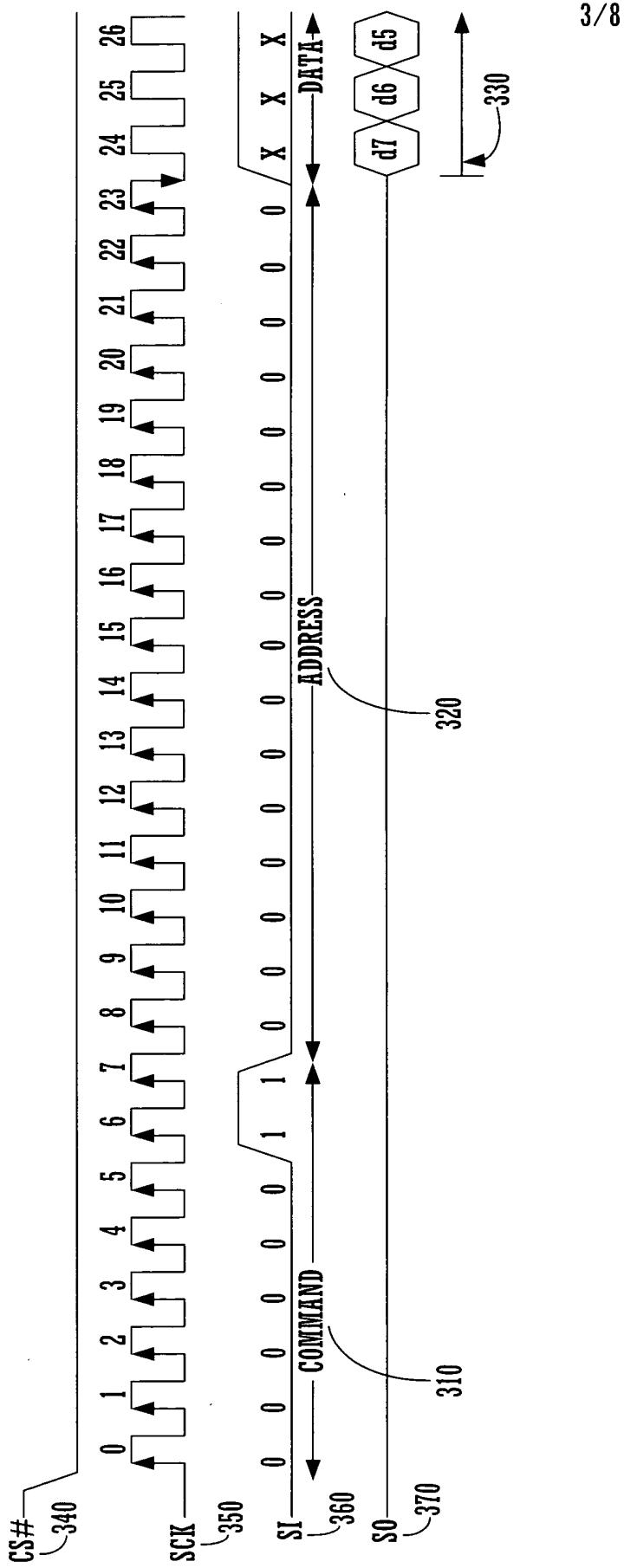


FIGURE 3

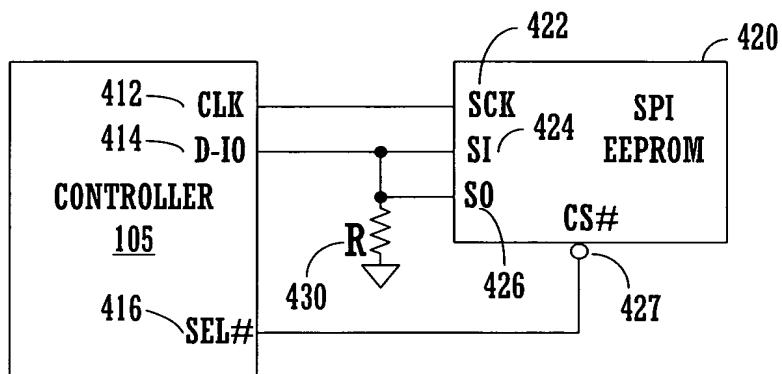
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**Replacement Sheet**

400



**FIGURE 4**



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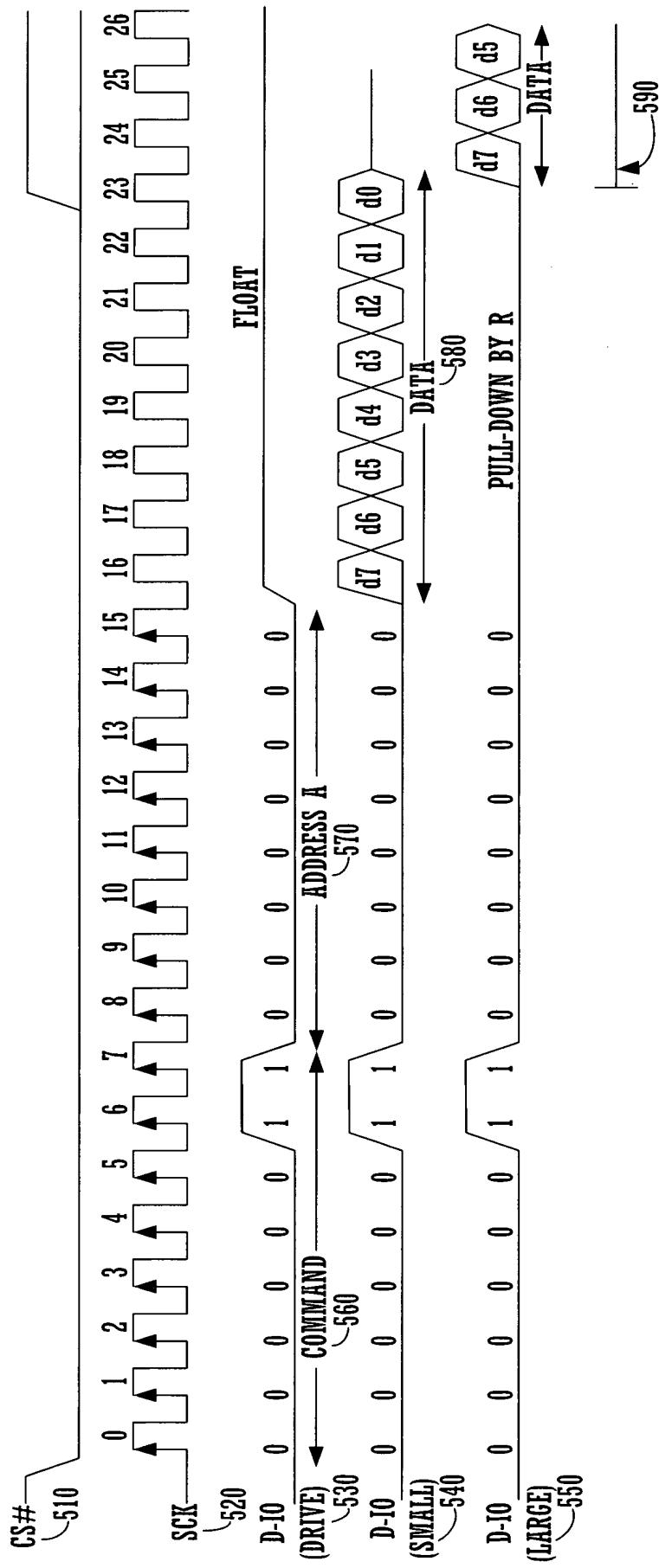


FIGURE 5



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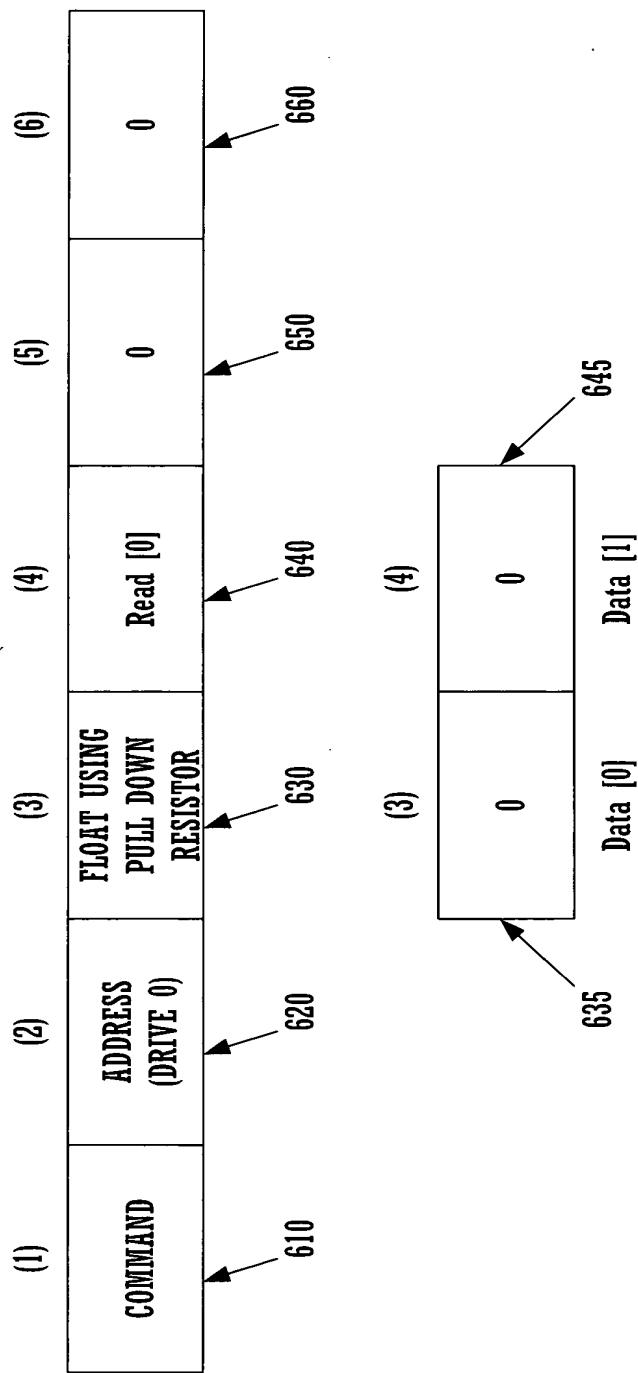


FIGURE 6



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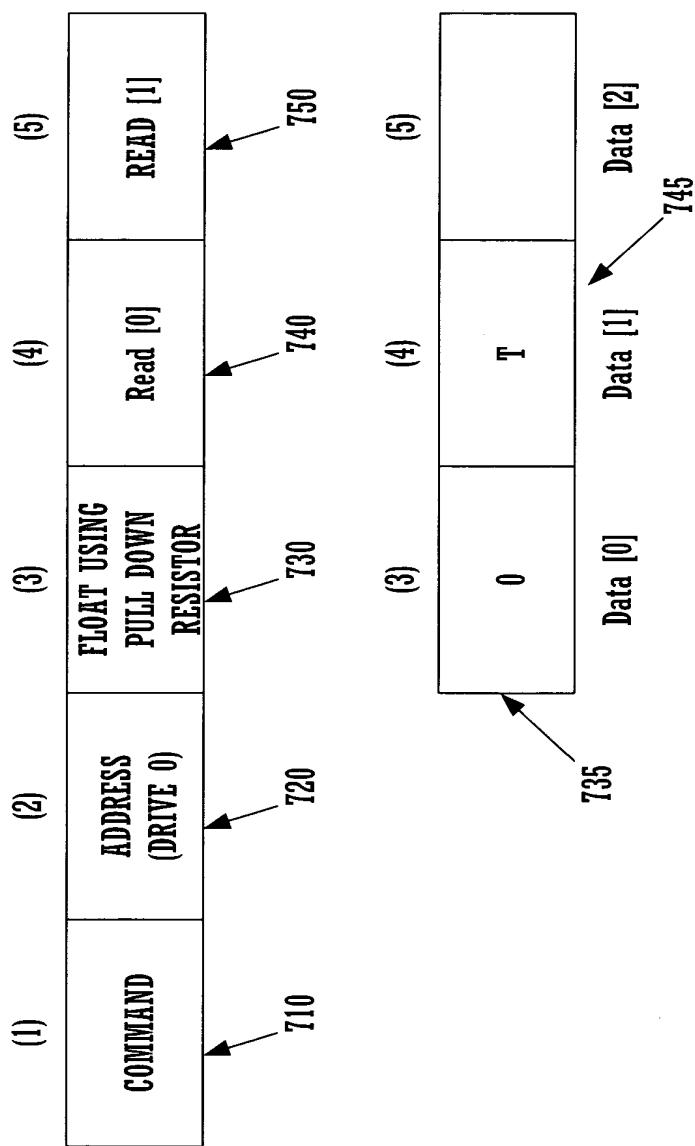


FIGURE 7



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800

START

SEND THE READ COMMAND BYTE.

810

DRIVE THE DATA INPUT/OUTPUT PIN LOW FOR EIGHT SERIAL CLOCK PULSES.

820

FLOAT THE DATA INPUT/OUTPUT PIN.

830

WEAKLY PULL DOWN THE DATA INPUT/OUTPUT PIN TO A LOGIC "0" LEVEL.

840

DETECT THE PRESENCE OF ZERO OR NON-ZERO BYTE VALUES AT THE DATA INPUT/OUTPUT PIN OF THE CONTROLLER TO DETERMINE THE PRESENCE OF A SMALL, LARGE, OR LARGER SPI EEPROM, OR ALTERNATIVELY, THE ABSENCE OF AN SPI EEPROM.

850

END

FIGURE 8